Form 1449 (Modified)	Atty. Docket No.	Serial No.:
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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

	Document	Publication	Country or		Sub-	Trans	lation
No.	No.	Date	Patent Office	Class	class	Yes	No
Α	WO 99/65219	12/16/99	PCT	H04M	11/06	Х	
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**Other Documents** 

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Olicia Baturay

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Form 1449 (Modified PATE TRADE

Atty. Docket No. NVIDP322/P001314 Application No.: 10/049,972

Information Disclosure

Applicant:

Statement By Applicant

Michael Ward Johnson et al.

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Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	A	5,012,489	4/30/1991	Burton et al.	375	8	11/7/1998
	В	5,161,193	11/3/1992	Lampson et al.	380	49	6/29/1990
	C	5,163,131	11/10/1992	Row et al.	395	200	9/8/1989
	D	5,303,344	4/12/1994	Yokoyama et al.	395	200	2/25/1991
	E	5,307,413	4/26/1994	Denzer	380	49	7/19/1991
	F	5,426,694	6/20/1995	Herbert	379	242	10/8/1993
	G	5,430,727	7/4/1995	Callon	370	85.13	5/19/1994
	Н	5,440,551	8/8/1995	Suzuki	370	60	1/4/1994
	I	5,485,460	1/16/1996	Schrier et al.	370	94.1	8/19/1994
	J	5,495,480	2/27/1996	Yoshida	370	60	6/21/1994
	K	5,499,353	3/12/1996	Kadlec et al.	395	445	6/20/1995

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

## Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
AB	М	Chesson, Greg, "The Protocol Engine Project" echnology Focus September 1987				
AG	N	Chesson, Greg, "Proceedings of the Summer 1987 USENIX Conference" USENIX Association June 8-12, 1987				
AB	0	G. Chesson and L. Green, "XTP Protocol Engine VLSI for Real-Time LANS" EFOC/LAN June 29-July 1, 1968				
AB	P	Stevens, Richard W., "TCP/IP Illustrated Volume" Addison-Wesley Professional Computing Series				
AB	Q	Abbot, Mark B., and Peterson, Larry L., "Increasing Network Throughput by Integrating Protocol Layers" IEEE 1993				
Examiner (	Uli	ia Baturay Date Considered 10/11/2005				

Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972			
Information Disclosure	Applicant:				
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Examiner				•		Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
ALS	Α	5,513,324	4/30/1996	Dolin, Jr. et al.	395	200.18	3/7/1994
	В	5,519,704	3/21/1996	Farinacci et al.	370	85.13	4/21/1994
	С	5,546,453	8/13/1996	Herbert	379	242	6/7/1995
	D	5,566,170	10/15/1996	Bakke et al.	370	60	12/29/1994
	E	5,577,105	11/19/1996	Baum et al.	379	93 ·	3/11/1994
	F	5,577,172	11/19/1996	Vatland et al.	395	114	8/31/1994
	G	5,577,237	11/19/1996	Lin	395	555	1/23/1995
	Н	5,598,410	1/28/1997	Stone	370	469	12/29/1994
	I	5,619,650	4/8/1997	Bach et al.	395	200.01	9/21/1995
	J	5,625,678	4/29/1997	Blomfield-Brown	379	93	5/24/1995
	K	5,625,825	4/29/1997	Rostoker et al.	395	730	10/21/1993

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation	
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No	
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Other Documents

		Other Documents			
Examiner	Na	Austral Title Date Director Towns I) of Dutilization			
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
_	M	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes,			
AB	1	Autos, and Offices"			
		EDN July 20, 1992			
A 0	N	Preston, David J., "Internet Protocols Migrate to Silicon For Networking			
AB		Devices"			
		Electronic Design April 14, 1997			
•	0	Multer, Raimund, "LON - das universelle Netzwerk"			
		Electronik 22/1991			
40	P	Rang, Michael ad Tantawy, Ahmed, "A Design Methodology for Protocol			
<i>4</i> 43		Processors"			
_ ハン		IEEE 1995			
Examiner	<u> </u>	Date Considered			
Olicia Baturay   Bate Considered 10/11/2005					

Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
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			0101 1 41	cut Documents			
Examiner		_				Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	A	5,634,015	5/27/1997	Chang et al.	395	309	10/4/1994
	В	5,636,371	6/3/1997	Yu	395	500	6/7/1995
	C	5,640,394	6/17/1997	Schrier et al.	370	389	1/16/1996
	D	5,663,951	9/2/1997	Danneels et al.	370	230	6/6/1995
	E	5,666,362	9/9/1997	Chen et al.	370	420	7/25/1995
	F	6,575,507	10/7/1997	Bobo, II	364	514R	4/28/1995
	G	5,687,314	11/11/1997	Osman et al.	395	200.01	6/9/1992
	Н	5,696,899	12/9/1997	Kalwitz	395	200.1	11/18/1992
	I	5,699,350	12/16/1997	Kraslavsky	370	254	10/6/1995
	J	5,701,316	12/23/1997	Alferness et al.	371	53	8/31/1995
	K	5,727,149	3/10/1998	Hirata et al.	395	200.8	12/21/1995

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

Other Documents

Examiner					
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication .			
AB	R	Banks, David and Prudence, Michael, "A High-Performance Network Architecture for a PA-RISC Workstation" IEEE Journal Vol. II, No. 22 February 1993			
AB	S	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory Systems: Architecture and Applications"			
AB	ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997  T Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D.I.and Stassinopoulos, G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband Integrated Services Digital Network and Applications"  National Technical University of Athens, Greece				
Examiner (	Uli	cia Baturay Date Considered 10/11/2005			

Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
Information Disclosure	Applicant:	•
Statement By Applicant	Michael Ward Johnson et Filing Date:	al.  Group Art Unit:
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Examiner	T		T	- Documents	T	Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	Α	5,734,865	3/31/1998	Yu	395	500	6/27/1995
	В	5,748,905	5/5/1998	Hauser et al.	395	200.79	8/30/1996
	C	5,754,540	5/19/1998	Liu et al.	370	315	7/18/1995
	D	5,761,281	6/2/1998	Baum et al.	379	93.29	7/30/1996
	E	5,790,546	8/4/1998	Dobbins et al.	370	400	12/4/1995
	F	5,790,676	8/4/1998	Ganesan et al.	380	23	9/23/1997
	G	5,802,278	9/1/1998	Isfeld et al.	395	200.02	1/23/1996
	Н	5,802,306	9/1/1998	Hunt	395	200.58	10/31/1995
	I	5,805,816	9/8/1998	Picazo, Jr. et al.	395	200.53	1/28/1997
	J.	5,809,235	9/15/1998	Sharma et al.	395	200.6	3/8/1996
	K	5,815,516	9/29/1998	Aaker et al.	371	53	4/5/1996

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

# Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
AB	R	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, "A Study of Virtual Memory MTU Reassembly withing the PowerPC Architecture" IEEE 1997			
AB	S	Steenkiste, Peter, "A Systematic Approach to Host Interface Design for High-Speed Networks" EEE March 1994			
AB	T	Wittie, Larry D., Ma, Fanyuan, "A TCP/IP Communication Subsystem in Micros" IEEE 1987			
Examiner	al	ricia Baturay Date Considered 10/11/2005			

Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.:		
Information Disclosure	Applicant:	10/049,972		
Statement By Applicant	Michael Ward Johnson et al.			
	Filing Date:	Group Art Unit:		
(Use Several Sheets if Necessary)	02/13/2002	-2661-2155		

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class ·	Date
AB	A	5,818,935	10/6/1998	Maa	380	20	3/10/1997
	В	5,826,032	10/20/1998	Finn et al.	395	200.66	2/12/1996
	C	5,872,919	2/16/1999	Wakeland	395	200.6	5/7/1997
	D	5,894,557	4/13/1999	Bade et al.	395	200.58	3/29/1996
	Е	5,909,546	6/1/1999	Osborne	395	200.42	3/8/1996
	F	5,920,732	7/6/1999	Riddle	395	876	7/1/1996
	G	5,935,268	8/10/1999	Weaver	714	758	6/3/1997
	Н	5,937,169	8/10/1999	Connery et al.	395	200.8	10/29/1997
	I	5,941,988	8/24/1999	Bhagwat et al.	713	201	1/27/1997
	J	5,943,481	8/24/1999	Wakeland	395	200.6	5/7/1997
	K	5,974,518	10/26/1999	Nogradi	711	173	4/10/1997

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

# Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
AB	R	Dalton, Chris; Watson, Greg; Banks, David; Calamvokis, Costas; Edwards, Aled; Lumley, John, "Afterburner: A Network-independent card provides rehitectural support for high-performance protocols" EEE July 1993			
AB	S	Gupta, Pankaj; McKeown, Nick, "Algorithms for Packet Classification" IEEE Network March/April 2001			
AB	Т	Clark, David D.; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE 1988			
Examiner (	)li	in Baturay Date Considered 10/11/2005			

Form 1449 (Modified)	Atty. Docket No.	Application No.:		
•,	NVIDP322/P001314	10/049,972		
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Statement By Applicant	Michael Ward Johnson et al.			
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Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	Α	5,999,974	12/7/1999	Ratcliff et al.	709	224	8/29/1997
	В	6,014,699	1/11/2000	Ratcliff et al.	709	224	8/29/1997
	C	6,034,963	3/7/2000	Minami et al.	370	401	10/31/1996
	D	6,046,980	4/4/2000	Packer	370	230	11/24/1997
	E	6,061,742	5/9/2000	Stewart et al.	709	250	10/10/1997
	F	6,076,115	6/13/2000	Sambamurthy et al.	709	250	4/24/1997
	G	6,081,846	6/27/2000	Hyder et al.	709	250	5/8/1997
	Н	6,092,110	7/18/2000	Maria et al.	709	225	10/23/1997
	I	6,092,229	7/18/2000	Boyle et al.	714	748	10/9/1996
	J	6,098,188	8/1/2000	Kalmanek, Jr. et al.	714	746	6/30/1995
	K	6,101,543	8/8/2000	Alden et al.	709	229	10/25/1996

Foreign Patent or Published Foreign Patent Application

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Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

# Other Documents

Examiner				
Initial	No.	author, Title, Date, Place (e.g. Journal) of Publication		
AB	R	Clark, David D.; Jacobson, Van; Romkey, John; Salwen, Howard, "An		
//-	ļ	Analysis of TCP Processing Overhead"		
		IEEE June 1989		
Δ -	S	Goloi, W.K.; Behr, P. "An IPC Protocol and Its Hardware Realization For A		
AB		High-Speed Distributed Multicomputer System"		
<u> </u>	<u> </u>	IEEE 1981		
AB	T	Ames, Richard, "Building an Embedded Web Server from Scratch"		
110		Circuit Cellar INK February 1998		
Examiner		Date Considered		
(		Cia Baturay Date Considered 10/11/05		

Form 1449 (Modified)	Atty. Docket No.	Application No.:		
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Examiner		•				Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	Α	6,151,625	11/21/2000	Swales et al.	709	218	4/30/1999
	В	6,157,955	12/5/2000	Narad et al.	709	228	6/15/1998
	С	6,172,980	1/9/2001	Flanders et al.	370	401	9/11/1997
	D	6,172,990	1/9/2001	Deb et al.	370	474	11/12/1997
	E	6,173,333	1/9/2001	Jolitz et al.	709	240	1/28/1999
	F	6,182,228	1/30/2001	Boden et al.	713	201	8/17/1998
	G	6,185,619	2/6/2001	Joffe et al.	709	229	11/7/1997
	Н	6,230,193	5/8/2001	Arunkumar et al.	709	218	10/31/1996
	I	6,233,626	5/15/2001	Swales et al.	710	11	10/6/1998
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Foreign Patent or Published Foreign Patent Application

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Other Documents

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Examiner					
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
AB	R	Legg, John, "Choosing and implementing an embedded TCP/IP Stack" Electronic Product Design January 1999			
AB	S	rphanos, George; Birbas, Alexios; Petrellis, Nikos; Mountzouris, Ioannis; lalataras, Andreas, "Compensating for Moderate Effective Throughput at e Desktop" EEE Communication Magazine April 2000			
AB	Т	Yocum, Kenneth G.; Chase, Jeffrey S.; Gallatin, Andrew J.; Lebeck, Alvin R., "Cut-Through Delivery in Trapeze: An Exercise in Low-Latency Messaging IEEE 1997			
Examiner Ċ					

Form 1449 (Modified)	Atty. Docket No.	Application No.:		
•	NVIDP322/P001314	10/049,972		
Information Disclosure	Applicant:			
Statement By Applicant	Michael Ward Johnson et al.			
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Foreign Patent or Published Foreign Patent Application

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# Other Documents

Examiner					
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
AB	R	Varada, S.; Yang, Y.; Evans, D., "Data and Buffer Management in ATM Systems"  TranSwitch Corporation			
AB	S	Bonjour, Dominique; de Hauteclocque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters" IEEE 1998			
AB	T	Kim, Chan; Jun, Jong-Jun; Park, Yeong-Ho; Lee, Kyu-Ho; Kim, Hyup-Jong, "Design and Implementation of a High-Speed ATM Host Interface Controller"  Electronics and Telecommunications Research Institute, Korea			
AB		Steenkiste, Peter, "Design, Implementation, and evaluation of a Single-copy Protocol Stack"  Software – Practice and Experience, Vol. 28, June 1998			
AB		Meleis, Hanafy E.; Serpanos, Dimitrios, N., "Designing Communication Subsystems for High-Speed Networks" IEEE Network July 1992			
Examiner (	H	cia Baturay Date Considered 10/11/05			

Atty. Docket No.	Application No.:			
NVIDP322/P001314	10/049,972			
Applicant:				
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# Other Documents

Examiner						
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
AB	R	Doumenis, Gr. A.; Reisis, D.I.; Stassinopoulos, G.I., "Efficient Implementation of the SAR Sublayer and the ATM Layer in High-Speed Broadband ISDN Data Terminal Adapters" IEEE 1993				
AB	S	Mora, F.; Sebastia, A., "Electronic Design of a High Performance Interfacce to the SCI Network" IEEE 1998				
AB	T	Eady, Fred, "Embedded Internet Part 2: TCP/IP and a 16-Bit Compiler" Embedded PC June 1999				
AB		Shivam, Piyush; Wyckoff, Pete; Panda, Dhabaleswar, "EMP: Zero-copy OS-bypass NIC-driven Gigabit Ethernet" SC2001 November 2001, Denver CO, USA				
AB		Mansour, Mohammad; Kayssi, Ayman, "FPGA-Based Internet Protocol Version 6 Router" IEEE 1998				
AB	Smith, Jonathon M.; Traw, C. Brendan S., "Giving Applications Access to					
AB		Traw, C. Brendan S.; Smith, Jonathan M., "Hardware/Software Organization of a High-Performance ATM Host Interface" IEEE 1993				
Examiner (	Ili	cia Baturay Date Considered 10/11/2005				

Form 1449	(Mod	ified)		Atty. Docket No. Application No.: NVIDP322/P001314 10/049,972				lo.:	
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	R			, Yamashita, Hiroyul	kl, "High	-Perfort	mance		Not in English
		TCP/IP/ATM	•						English
	+-			munication Systems					
NO	S			, Yasuhiro; Yamahsi					
AB		Various Mult		1 Communication Bo	ards:Dn	ving For	ce for		
		Various Mult Vol. 9 No. 6		<del>-</del>					
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۸.0	1	4	·-	• .	-		uictio	ii aiiu	
AB		1	forward-error correction for very high-speed data transfer"  Dr. Dobbs Journal, August 1992						
^ ^			Chiswell, Dave "Implementation Challenges for 155Mbit ATM Adapters"						
AB	ł	ISBN# 0-7803-2636-9							
۸۸	<del>-  </del>	Wright, Maury "Intelligent Ethernet Boards"							
AB	1		EDN June 23, 1988						
	1			rotocols Migrate to S	Silicon Fo	or Netw	orking	<u> </u>	1
AB		Devices"		•					
110	Electronic Design April 14, 1997								
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Form 1449 (Modified)	Atty. Docket No.	Application No.:				
•	NVIDP322/P001314	10/049,972				
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U.S. Patent Documents						
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Foreign Patent or Published Foreign Patent Application

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1	Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
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Other Documents

Examiner						
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
00	R	Ivanov-Loshkanov, V.S.; Sevast'yanov, S.F., Semenov, M.N., "Network				
AB		Microprocessor Adapter"				
	ļ	Avtmatika i Vyshislitel'naya Tekhnika Vol. 17 No. 5 pp. 25-28, 1983				
10	S	Druschel, Peter; Abbot, Mark B.; Pagels, Michael A.; Peterson, Larry L.,				
AB		"Network Subsystem Design"				
		IEEE Network July 1993				
l wa	Т	Huange, Jau-Hsiung; Chen, Chi-Wen, "On Performance Measurments of				
AB		TCP/IP and its Device Driver"				
,	ļ	EEE 1992				
		Siegel, Martin; Williams, Mark; Robler, Georg, "Overcoming Bottlenecks in				
AB		High-Speed Transport Systems"				
( , -	ļ	IEEE 1991				
• •		Neufeld, Gerald W.; Ito, Mabo Robert; Goldberg, Murray; McCutcheon,				
1 A15		Mark J.; Ritchie, Stuart, "Paralleel Host Interface for an ATM Network				
		IEEE Network July 1993				
		Maly, K.; Khanna, K.; Kukkamala, R.; Overstreet C.M.; Yerraballi, R.;				
AB		Foundriat, E.C.; Madan, B., "Parallel TCP/IP For Multiprocessor				
1 , , , ,		Workstations"				
		High Performance Netowrking, IV, 1993 IFIP				
Examiner	Examiner Olicia Rational Date Considered 10/11/05					
		- Dannay				

Form 1449 (Modified) Information Disclosure Statement By Applicant				Atty. Docket No NVIDP322/P0 Applicant: Michael Ward J	01314	Applicati 10/049,9	
(Use S	everal	Sheets if Nec	essary)	Filing Date: 02/13/2002		Group A	n Unit: 155
			U.S. 1	Patent Documents			
Examiner						Sub-	Filing

Foreign Patent or Published Foreign Patent Application								
	Α							
Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Piling Date	

Examiner Initial No. No. Date Publication Country or Patent Office Class Class Yes No.

Other Documents

		Other Documents				
Examiner						
Initial	No.					
	R	Laskman, T.V.; Madhow, U., "Performance Analysis of Window-based Flow Control using TCP/IP:Effect of High Bandwidth Delay Products and				
A15		Random Loss"				
		High Performance Networking V. 1994 IFIP				
AC	S	Ramakrishnan, K.K., "Performance Considerations in Designing Network				
He		Interfaces"				
		IEEE Journal 1993				
• •	T	Camarda, P.; Pipio, F.; Piscitelli, G.; "Performance evaluating of TCP/IP				
AB		implementations in end systems"				
		IEE Proc-Computer Digital Tech. Vol. 146 No. 1 January 1999				
		Toyoshima, Kan; Shirakawa, Kazuhiro; Hayashi, Kazuhiro, "Programmable				
NA		ATM Adapter: Rapid Prototyping of Cell Processing Equipment for ATM				
70		Network"				
		IEEE 1997				
h O		Blumrich, Matthias A.; Dubnicku, Cezary; Felton, Edward W.; Li, Kai,				
AB		"Protected, User-level DMA for the SHRIMP Network Interface"				
		IEEE 1996				
Feldmeier, David C.; McAuley, Anthony J.; Smith, Jonathan M						
1 HB		Deborah S.; Marcus, William S.; Raleigh, Thomas M., "Protocol Boosters"				
IEEE 1998						
Examiner C	Examiner Olicin Baturay Date Considered 10/11/2005					
		cia Baturay 10/11/2005				

Form 1449 (Modified)	Atty. Docket No.	Application No.:
•	NVIDP322/P001314	10/049,972
Information Disclosure	Applicant:	
Statement By Applicant	Michael Ward Johnson et	al.
	Filing Date:	Group Art Unit:
(Use Several Sheets if Necessary)	02/13/2002	<del>-2661</del> 2155

Examiner Initial	No	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

# Other Documents

Examiner							
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication					
AB	R	Marcus, William S.; Hadzic, Ilija; McAuley, Anthony J.; Smith, Jonathan M., "Protocol Boosters: Applying Programmability to Network infrastructures"  EEE Communications Magazine October 1998					
AB	S	Korablum, Deborah F., "Protocol Implementation and Other Performance Issues for Local and Metropolitan Area Networks" IEEE 1988					
AB	T	Dittia, Zubin D.; Parulkar, Guru M.; Jr., Jerome R. Cox, "The APIC Approach to High Performance Network Interface Design: Protect4ed DMA and Other Techniques" IEEE 1997					
AB		Rutsche, Erich, "The Architecture of a Gb/s Multimedia Protocol Adapter" ACM SIGCOMM Computer Communication Review					
AB		Moldeklev, Kjersti; Klovning, Espen; Kure, Oivind, "The effect of end system hardware and software on TCP/IP throughput performance over a local ATM Network"					
AB		Kanakia, Hermant; Cheriton, David R., "The VMP Network Adapter Board (NAB) High Performance Network Communication for Multiprocessors" ACM 1988					
Examiner (	lle	Eia Baturey Date Considered 10/11/2005					

Form 1449 (Modified)				Atty. Docket No. Application No.: NVIDP322/P001314 10/049,972				0.:
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			U.S. Pate	nt Documents				
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Examiner		Document	Publication	Country or		Sub-	Tran	slation
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	J							
			Other	Documents				
Examiner								
Initial	No.	Author, Title	, Date, Place	(e.g. Journal) of Pub	lication			
	K	Chandramme	non, Grish P	.; Varghese, George,	"Trading	Packet	Heade	rs for
AB		Packet Proce	ssing"					
134	•	IEEE 1996						
AB	L	-	Michael J.K.,	"TURBOchannel"				
HID		IEEE 1991	,					
	M			newmedianews.com/			er.htm	1
AB	N		•	Hardware that Fits in	•	ing"		
אט				news/1998/77/ns-59				
ΛD	0		l. "Matsushit	a Launches WebTV	Internet (	Connecti	on	
AB		Terminal"						
			<del></del>	official.data.dir/en9	81112-1/	en98111	2-1htr	nl
AB	P	iReady Produ	ict Data Shee	t, Internet Tuner	<del> </del>			•
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Information Disclosure Statement By Applicant

(Use Several Sheets if Necessary)

Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

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Michael Ward Johnson et al.

Filing Date:

Group Art Unit:

02/13/2002

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## U.S. Patent Documents

Examiner			·			Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	A	5,012,489	4/30/1991	Burton et al.	375	8	11/7/1998
	В	5,161,193	11/3/1992	Lampson et al.	380	49	6/29/1990
	C	5,163,131	11/10/1992	Row et al.	395	200	9/8/1989
	D	5,303,344	4/12/1994	Yokoyama et al.	395	200	2/25/1991
	E	5,307,413	4/26/1994	Denzer	380	49	7/19/1991
	F	5,426,694	6/20/1995	Herbert	379	242	10/8/1993
	G	5,430,727	7/4/1995	Callon	370	85.13	5/19/1994
	H	5,440,551	8/8/1995	Suzuki	370	60	1/4/1994
·	1	5,485,460	1/16/1996	Schrier et al.	370	94.1	8/19/1994
	J	5,495,480	2/27/1996	Yoshida	370	60	6/21/1994
	K	5,499,353	3/12/1996	Kadlec et al.	395	445	6/20/1995

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

#### Other Documents

		Other Documents				
Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
AB	M	Chesson, Greg, "The Protocol Engine Project" Technology Focus September 1987				
	N	Chesson, Greg, "Proceedings of the Summer 1987 USENIX Conference" USENIX Association June 8-12, 1987				
O G. Chesson and L. Green, "XTP Protocol Engine VLSI for Real-Tim LANS" EFOC/LAN June 29-July 1, 1968						
	P	Stevens, Richard W., "TCP/IP Illustrated Volume" Addison-Wesley Professional Computing Series				
Q Abbot, Mark B., and Peterson, Larry L., "Increasing Network Throughpu Integrating Protocol Layers" IEEE 1993						
Examiner (	Uli	cia Baturay Date Considered 10/11/2005				

AUG 0 5 200 P

Form 1449 (Modified)

Atty. Docket No. NVIDP322/P001314 Application No.: 10/049,972

Information Disclosure Statement By Applicant

Disclosure Applicant:

Michael Ward Johnson et al.

(Use Several Sheets if Necessary) Filing Date: 02/13/2002

Group Art Unit: 2661

U.S. Patent Documents

Examiner					T	Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	Α	5,513,324	4/30/1996	Dolin, Jr. et al.	395	200.18	3/7/1994
	В	5,519,704	3/21/1996	Farinacci et al.	370	85.13	4/21/1994
	C	5,546,453	8/13/1996	Herbert	379	242	6/7/1995
	D	5,566,170	10/15/1996		370	60	12/29/1994
	E	5,577,105	11/19/1996	Baum et al.	379	93	3/11/1994
	F	5,577,172	11/19/1996	Vatland et al.	395	114	8/31/1994
	G	5,577,237	11/19/1996	Lin	395	555	1/23/1995
	H	5,598,410	1/28/1997	Stone	370	469	12/29/1994
	I	5,619,650	4/8/1997	Bach et al.	395	200.01	9/21/1995
	J	5,625,678	4/29/1997	Blomfield-Brown	379	93	5/24/1995
	K	5,625,825	4/29/1997	Rostoker et al.	395	730	10/21/1993

Foreign Patent or Published Foreign Patent Application

	Torciga Tatent of Tubusued Poteign Tatent Application										
Examiner		Document	Publication	Country or		Sub-	Trans	lation			
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No			
	L										

Other Documents

		CHIT. DOCUMENT
Examiner		
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AG	M	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices" EDN July 20, 1992
	N	Preston, David J., "Internet Protocols Migrate to Silicon For Networking Devices"  Electronic Design April 14, 1997
	10	Multer, Raimund, "LON – das universelle Netzwerk"
		Electronik 22/1991
AB	P	Rang, Michael ad Tantawy, Ahmed, "A Design Methodology for Protocol Processors" IEEE 1995
Examiner /	71	Date Considered 10/11/2009

Not in English

Examiner: Initial citation considered Deputies through situation if not in a fine situation is not in a fine situation in a fine situation is not in a fine situation in a fine situation in a fine situation in a fine situation is not in a fine situation in a



Form 1449 (Modified)

Atty. Docket No. NVIDP322/P001314 Application No.:

Information Disclosure Statement By Applicant

(Use Several Sheets if Necessary)

Applicant:

10/049,972

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Filing Date:

Group Art Unit:

02/13/2002

2661

U.S. Patent Documents

C.S. I accut Documents									
Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date		
AB	Α	5,634,015	5/27/1997	Chang et al.	395	309	10/4/1994		
	В	5,636,371	6/3/1997	Yu .	395	500	6/7/1995		
	C	5,640,394	6/17/1997	Schrier et al.	370	389	1/16/1996		
	D	5,663,951	9/2/1997	Danneels et al.	370	230	6/6/1995		
	E	5,666,362	9/9/1997	Chen et al.	370	420	7/25/1995		
	F	6,575,507	10/7/1997	Bobo, II	364	514R	4/28/1995		
	G	5,687,314	11/11/1997	Osman et al.	395	200.01	6/9/1992		
	Н	5,696,899	12/9/1997	Kalwitz	395	200.1	11/18/1992		
	I	5,699,350	12/16/1997	Kraslavsky	370	254	10/6/1995		
	J	5,701,316	12/23/1997	Alferness et al.	371	53	8/31/1995		
	K	5,727,149	3/10/1998	Hirata et al.	395	200.8	12/21/1995		

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
,	L							

### Other Documents

	~~	O HOT DOUBLING				
Examiner						
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
	M	Banks, David and Prudence, Michael, "A High-Performance Network				
AB		Architecture for a PA-RISC Workstation"				
リ		IEEE Journal Vol. II, No. 22 February 1993				
	N	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory				
1.		Systems: Architecture and Applications"				
		ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997				
	0	Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D.I. and Stassinopoulos,				
.		G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband				
l	1	Integrated Services Digital Network and Applications"				
National Technical University of Athens, Greece						
Examiner (	70,	Date Considered 10/11/05				

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Form 1449 (Modified)

Information Disclosure Statement By Applicant Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

Applicant:

Michael Ward Johnson et al.

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Group Art Unit:

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(Use Several Sheets if Necessary) Filing Date: 02/13/2002

U.S. Patent Documents

C.S. 1 atent Documents									
Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date		
AG	A	5,734,865	3/31/1998	Yu	395				
- F C	<del></del>					500	6/27/1995		
	B	5,748,905	5/5/1998	Hauser et al.	395	200.79	8/30/1996		
	C	5,754,540	5/19/1998	Liu et al.	370	315	7/18/1995		
	D	5,761,281	6/2/1998	Baum et al.	379	93.29	7/30/1996		
	E	5,790,546	8/4/1998	Dobbins et al.	370	400	12/4/1995		
	F	5,790,676	8/4/1998	Ganesan et al.	380	23	9/23/1997		
	G	5,802,278	9/1/1998	Isfeld et al.	395	200.02	1/23/1996		
	H	5,802,306	9/1/1998	Hunt .	395	200.58	10/31/1995		
	I	5,805,816	9/8/1998	Picazo, Jr. et al.	395	200.53	1/28/1997		
	J	5,809,235	9/15/1998	Sharma et al.	395	200.6	3/8/1996		
	K	5,815,516	9/29/1998	Aaker et al.	371	53	4/5/1996		

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

Other Documents

		Other Documents						
Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication						
AB	М	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, "A Study of Virtual Memory MTU Reassembly withing the PowerPC Architecture" IEEE 1997						
	N	teenkiste, Peter, "A Systematic Approach to Host Interface Design for ligh-Speed Networks" EEE March 1994						
	0	Wittie, Larry D., Ma, Fanyuan, "A TCP/IP Communication Subsystem in Micros" IEEE 1987						
Examiner	01	Date Considered						

Examiner alicen Baturay Date Considered 10/14/2005

1449 (Modified)

Information Disclosure Statement By Applicant

(Use Several Sheets if Necessary)

Atty. Docket No. NVIDP322/P001314 Application No.: 10/049,972

Applicant:

Michael Ward Johnson et al.

Filing Date:

Group Art Unit:

02/13/2002

2661

U.S. Patent Documents

. C.S. I atent Documents									
Examiner						Sub-	Filing		
Initial	No.	Patent No.	Date	Patentee	Class	class	Date		
A:3	A	5,818,935	10/6/1998	Maa	380	20	3/10/1997		
	В	5,826,032	10/20/1998	Finn et al.	395	200.66	2/12/1996		
	C	5,872,919	2/16/1999	Wakeland	395	200.6	5/7/1997		
	D	5,894,557	4/13/1999	Bade et al.	395	200.58	3/29/1996		
	E	5,909,546	6/1/1999	Osborne	395	200.42	3/8/1996		
	F	5,920,732	7/6/1999	Riddle	395	876	7/1/1996		
	G	5,935,268	8/10/1999	Weaver	714	758	6/3/1997		
	Н	5,937,169	8/10/1999	Connery et al.	395	200.8	10/29/1997		
	I	5,941,988	8/24/1999	Bhagwat et al.	713	201	1/27/1997		
	J	5,943,481	8/24/1999	Wakeland	395	200.6	5/7/1997		
	K	5,974,518	10/26/1999	Nogradi	711	173	4/10/1997		

Foreign Patent or Published Foreign Patent Application

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Examiner		Document.	Publication	Country or		Sub-	Trans	lation	۱
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	L								ĺ

Other Documents

Exam									
Initia	<u> </u>	No.	uthor, Title, Date, Place (e.g. Journal) of Publication						
Dalton, Chris; Watson, Greg; Banks, David; Calamvokis, Costas; Edward Aled; Lumley, John, "Afterburner: A Network-independent card provides architectural support for high-performance protocols"  IEEE July 1993									
		N	Gupta, Pankaj; McKeown, Nick, "Algorithms for Packet Classification" IEEE Network March/April 2001						
O Clark, David D.; Romkey, John; Salwen, Howard, "An Analysis of " Processing Overhead" IEEE 1988									
Examiner Olicia Batting Date Considered 10/11/2005									



449 (Modified)

Information Disclosure Statement By Applicant Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

Applicant:

Michael Ward Johnson et al.

Filing Date: 02/13/2002

Group Art Unit:

2661

(Use Several Sheets if Necessary)

U.S. Patent Documents

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
AB	A	5,999,974 .	12/7/1999	Ratcliff et al.	709	224	8/29/1997
	В	6,014,699	1/11/2000	Ratcliff et al.	709	224	8/29/1997
	C	6,034,963	3/7/2000	Minami et al.	370	401	10/31/1996
	D	6,046,980	4/4/2000	Packer	370	230	11/24/1997
	E	6,061,742	5/9/2000	Stewart et al.	709	250	10/10/1997
	F	6,076,115	6/13/2000	Sambamurthy et al.	709	250	4/24/1997
	G	6,081,846	6/27/2000	Hyder et al.	709	250	5/8/1997
	H	6,092,110	7/18/2000	Maria et al.	709	225	10/23/1997
	I	6,092,229	7/18/2000	Boyle et al.	714	748	10/9/1996
	J	6,098,188	8/1/2000	Kalmanek, Jr. et al.	714	746	6/30/1995
1	K	6,101,543	8/8/2000	Alden et al.	709	229	10/25/1996

Foreign Patent or Published Foreign Patent Application

		X OI VIGU X M	CHI OI I HOUSE	co l'ortigu i attut	i Applicati	VII		
Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	L							

Other Documents

Examiner	No.					
Initial	Author, Title, Date, Place (e.g. Journal) of Publication					
AB	М	Clark, David D.; Jacobson, Van; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE June 1989				
	N	Goloi, W.K.; Behr, P. "An IPC Protocol and Its Hardware Realization For A High-Speed Distributed Multicomputer System"  EEE 1981				
	0	Ames, Richard, "Building an Embedded Web Server from Scratch" Circuit Cellar INK February 1998				
Examiner	lle	cia Baturay Date Considered 10/11/2005				

Form 1449 (Modified)

Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

Information Disclosure Statement By Applicant . Applicant:

Michael Ward Johnson et al. Filing Date:

(Use Several Sheets if Necessary)

02/13/2002

Group Art Unit: 2661

U.S. Patent Documents

Examiner	T		1	Cht Documents		Sub-	True
Initial	No.	Patent No.	Date	Patentee	Class	class	Filing Date
AB	Α	6,151,625	11/21/2000	Swales et al.	709	218	4/30/1999
	В	6,157,955	12/5/2000	Narad et al.	709	228	6/15/1998
	C	6,172,980	1/9/2001	Flanders et al.	370	401	9/11/1997
	D	6,172,990	1/9/2001	Deb et al.	370	474	11/12/1997
	E	6,173,333	1/9/2001	Jolitz et al.	709	240	1/28/1999
	F	6,182,228	1/30/2001	Boden et al.	713	201	8/17/1998
	G	6,185,619	2/6/2001	Joffe et al.	709	229	11/7/1997
	H	6,230,193	5/8/2001	Arunkumar et al.	709	218	10/31/1996
	I	6,233,626	5/15/2001	Swales et al.	710	11	10/6/1998
\$	J						
3	K				<del> </del> -		

Foreign Patent or Published Foreign Patent Application

-	Examiner			Publication	Country or	<u> </u>	Sub-	Trans	lation
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		L			·				

Other Documents

Examiner							
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication					
AB	M	Legg, John, "Choosing and implementing an embedded TCP/IP Stack" Electronic Product Design January 1999					
	N	Orphanos, George; Birbas, Alexios; Petrellis, Nikos; Mountzouris, Ioannis; Malataras, Andreas, "Compensating for Moderate Effective Throughput at the Desktop"  IEEE Communication Magazine April 2000					
O Yocum, Kenneth G.; Chase, Jeffrey S.; Gallatin, Andrew J.; Lebeck, Al R., "Cut-Through Delivery in Trapeze: An Exercise in Low-Latency Messaging IEEE 1997							
Examiner							



Form 1449 (Modified)

Atty. Docket No. Application No.:

NVIDP322/P001314 10/049,972

Applicant:

Applicant:

Michael Ward Johnson et al.

Filing Date: Group Art Unit:

(Use Several Sheets if Necessary)

O2/13/2002 2661

U.S. Patent Documents

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Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	В							

Other Documents

	***************************************	Other Documents				
Examiner						
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
·Ain	С	Varada, S.; Yang, Y.; Evans, D., "Data and Buffer Management in ATM				
AB		Systems"				
		TranSwitch Corporation				
	D	Bonjour, Dominique; de Hauteclocque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters"				
	IEEE 1998					
	E	Kim, Chan; Jun, Jong-Jun; Park, Yeong-Ho; Lee, Kyu-Ho; Kim, Hyup-Jong, "Design and Implementation of a High-Speed ATM Host Interface Controller"				
		Electronics and Telecommunications Research Institute, Korea				
	F	Steenkiste, Peter, "Design, Implementation, and evaluation of a Single-copy				
		Protocol Stack"				
		Software - Practice and Experience, Vol. 28, June 1998				
	G	Meleis, Hanafy E.; Serpanos, Dimitrios, N., "Designing Communication				
		Subsystems for High-Speed Networks"				
		IEEE Network July 1992				
Examiner Clicia Baterian Date Considered 10/11/2005						
<u> </u>						

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Information Disclosure Statement By Applicant Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

Applicant:

Michael Ward Johnson et al.

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(Use Several Sheets if Necessary)

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner			Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
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Other Documents

	<del></del>	Total Documents					
Examiner							
Initial	No.	(3,000,000,000,000,000,000,000,000,000,0					
	C	Doumenis, Gr. A.; Reisis, D.I.; Stassinopoulos, G.I., "Efficient					
NA		Implementation of the SAR Sub layer and the ATM Layer in High-Speed					
AU		roadband ISDN Data Terminal Adapters" EEE 1993					
		IEEE 1993					
	D	Mora, F.; Sebastia, A., "Electronic Design of a High Performance Interface					
		to the SCI Network"					
		EEE 1998 .					
	E	Eady, Fred, "Embedded Internet Part 2: TCP/IP and a 16-Bit Compiler"					
		Embedded PC June 1999					
	F	Shivam, Piyush; Wyckoff, Pete; Panda, Dhabaleswar, "EMP: Zero-copy OS-					
		bypass NIC-driven Gigabit Ethernet"					
		SC2001 November 2001, Denver CO, USA					
	G	Mansour, Mohammad; Kayssi, Ayman, "FPGA-Based Internet Protocol					
		Version 6 Router"					
		IEEE 1998					
1	H	Smith, Jonathon M.; Traw, C. Brendan S., "Giving Applications Access to					
		Gb/s Networking"					
		IEEE Network July 1993					
	I	Traw, C. Brendan S.; Smith, Jonathan M., "Hardware/Software Organization					
		of a High-Performance ATM Host Interface"					
		IEEE 1993					
Examiner	al.	Date Considered 10/11/2005					
		Zen Batty 10/11/2005					



m 1449 (Modified)

Atty. Docket No. NVIDP322/P001314 Application No.:

Information Disclosure

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(Use Several Sheets if Necessary)

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2661

# U.S. Patent Documents

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
	Α						

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	В							

# Other Documents

Examiner								
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication						
•	e	Nagata, Takahiko, Hosoda, Yamashita, Hiroyuki, "High-Performance						
	1	TCP/IP/ATM Communication Board"						
		NTT Information and Communication Systems Laboratories 1998						
	D	Vagata, Takahiko; Hosoda, Yasuhiro; Yamahsita, Hiroyuki, "High- Performance TCP/IP/ATM Communication Boards: Driving Force for						
$\Lambda G$								
70		Various Multimedia Services"						
	Vol. 9 No. 6 November 1997							
	E	Jolitz, William Frederick, "High-Speed Networking: Header prediction and						
	1	forward-error correction for very high-speed data transfer"						
		Dr. Dobbs Journal, August 1992						
	F	Chiswell, Dave "Implementation Challenges for 155Mbit ATM Adapters"						
		ISBN# 0-7803-2636-9						
	G	Wright, Maury "Intelligent Ethernet Boards"						
		EDN June 23, 1988						
	H	Preston, David "Internet Protocols Migrate to Silicon For Networking						
Devices"								
Electronic Design April 14, 1997								
Examiner alicia Baturay Date Considered 10/11/2005								
	we	ca Darriay   10/11/2000						

n 1449 (Modified)

Information Disclosure

Statement By Applicant

(Use Several Sheets if Necessary)

Atty. Docket No. NVIDP322/P001314 Application No.: 10/049,972

Applicant:

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Filing Date:

Group Art Unit:

02/13/2002

2661

**U.S. Patent Documents** 

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	В				1			

Other Documents

		Other Documents
Examiner		,
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C	Ivanov-Loshkanov, V.S.; Sevast'yanov, S.F., Semenov, M.N., "Network
AB		Microprocessor Adapter:
V 12		Avtmatika i Vyshislitel'naya Tekhnika Vol. 17 No. 5 pp. 25-28, 1983
	D	Druschel, Peter; Abbot, Mark B.; Pagels, Michael A.; Peterson, Larry L.,
		"Network Subsystem Design"
		IEEE Network July 1993
1	E	Huange, Jau-Hsiung; Chen, Chi-Wen, "On Performance Measurements of
		TCP/IP and its Device Driver"
		IEEE 1992
	F	Siegel, Martin; Williams, Mark; Robler, Georg, "Overcoming Bottlenecks in
l		High-Speed Transport Systems"
	<u> </u>	IEEE 1991
	G	Neufeld, Gerald W.; Ito, Mabo Robert; Goldberg, Murray; McCutcheon,
		Mark J.; Ritchie, Stuart, "Parallel Host Interface for an ATM Network
		IEEE Network July 1993
	H	Maly, K.; Khanna, K.; Kukkamala, R.; Overstreet C.M.; Yerraballi, R.;
		Foundriat, E.C.; Madan, B., "Parallel TCP/IP For Multiprocessor
		Workstations"
	<u> </u>	High Performance Networking, IV, 1993 IFIP
Examiner (	105	Date Considered 10/11/2005

49 (Modified)

Information Disclosure Statement By Applicant Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

Applicant:

Michael Ward Johnson et al.

Filing Date:

Group Art Unit:

(Use Several Sheets if Necessary)

02/13/2002

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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or .		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	В							

Other Documents

-	<del></del>	~~~~					
Examin							
Initial		No.	Author, Title, Date, Place (e.g. Journal) of Publication				
A	3	С	Laskman, T.V.; Madhow, U., "Performance Analysis of Window-based Flow Control using TCP/IP: Effect of High Bandwidth Delay Products and Random Loss" High Performance Networking V. 1994 IFIP				
High Performance Networking V. 1994 IFIP  Ramakrishnan, K.K., "Performance Considerations in Designing Network Interfaces"  IEEE Journal 1993							
AP	E Camarda, P.; Pipio, F.; Piscitelli, G.; "Performance evaluating of TCP/IP implementations in end systems" IEE Proc-Computer Digital Tech. Vol. 146 No. 1 January 1999						
		F	Toyoshima, Kan; Shirakawa, Kazuhiro; Hayashi, Kazuhiro, "Programmable ATM Adapter: Rapid Prototyping of Cell Processing Equipment for ATM Network" IEEE 1997				
		G	Blumrich, Matthias A.; Dubnicku, Cezary; Felton, Edward W.; Li, Kai, "Protected, User-level DMA for the SHRIMP Network Interface" IEEE 1996				
		Н	Feldmeier, David C.; McAuley, Anthony J.; Smith, Jonathan M., Bakin, Deborah S.; Marcus, William S.; Raleigh, Thomas M., "Protocol Boosters" IEEE 1998				
Examin	er ()	le	ein Baturay Date Considered				

1449 (Modified)

Atty. Docket No. NVIDP322/P001314 Application No.: 10/049,972

Information Disclosure Statement By Applicant Applicant:

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Group Art Unit:

(Use Several Sheets if Necessary) 02/13/2002 2661

**U.S. Patent Documents** 

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A						

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	В							

Other Documents

Examiner	T	•				
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
Linual	-					
	C	Marcus, William S.; Hadzic, Ilija; McAuley, Anthony J.; Smith, Jonathan				
AA	ļ	M., "Protocol Boosters: Applying Programmability to Network				
		Infrastructures"				
		IEEE Communications Magazine October 1998				
	D	Korablum, Deborah F., "Protocol Implementation and Other Performance				
		Issues for Local and Metropolitan Area Networks"				
1		IEEE 1988				
	Е	Dittia, Zubin D.; Parulkar, Guru M.; Jr., Jerome R. Cox, "The APIC				
		Approach to High Performance Network Interface Design: Protect4ed DMA				
1		and Other Techniques"				
		IEEE 1997				
	F	Rutsche, Erich, "The Architecture of a Gb/s Multimedia Protocol Adapter"				
		ACM SIGCOMM Computer Communication Review				
	G	Moldeklev, Kjersti; Klovning, Espen; Kure, Olvind, "The effect of end				
		system hardware and software on TCP/IP throughput performance over a				
		local ATM Network"				
	H	Kanakia, Hermant; Cheriton, David R., "The VMP Network Adapter Board				
1 /		(NAB) High Performance Network Communication for Multiprocessors"				
		ACM 1988				
Examiner	IL	econ Batura Date Considered 10/11/2005				

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m 1449 (Modified)

Atty. Docket No. NVIDP322/P001314

Application No.: 10/049,972

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Group Art Unit:

(Use Several Sheets if Necessary) 02/13/2

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# U.S. Patent Documents

Examiner						Sub-	Filing
Initial	No:	Patent No.	Date	Patentee	Class	class	Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	J					1		1

## Other Documents

		Other Documents					
Examiner		·					
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication					
A (2)		Chandrammenon, Grish P.; Varghese, George, "Trading Packet Headers for Packet Processing" IEEE 1996					
	L	Nielson, Dr. Michael J.K., "TURBOchannel" IEEE 1991					
	M	New Media News, www. newmedianews.com/02197/ts inettuner.html					
	N	Kelly, T. "Cheap Internet Hardware that Fits in Everything" ZDNet, www.zdnet.co.uk/news/1998/77/ns-5998.html					
	0	iReady Product Data Sheet, Internet Tuner					
	P	Kitadeya et al. "Matsushita Launches WebTV Internet Connection Terminal" www.mei.co.jp/corp/news/official.data.dir/en981112-1/en981112-1html					
	Q						
Examiner	lu	Date Considered 10/1/10007					

Form 1449 (Modified)	Atty. Docket No. NVIDP322/P001314	Application No.: 10/049,972
Information Disclosure Statement By Applicant	Applicant: Johnson et al.	
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Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Translation	
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
AB	L	01/013583A3	02/22/2001	WIPO	H04L	12/28	X	
1	M	99/39488	08/05/1999	WIPO	H04L	29/08	Х	
	N	99/65219	12/16/1999	WIPO	H04M	11/06	X	
	0							
	P			·				

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication		
AB	R	Margolin, Bob, "Smarter Stuff" http://www.byte.com/art/9706/sec6/art2.htm, June 1997		
	S	Panian et al. "The IP Modem Interface Standard: Draft" Portable Computer Communications Association, Brookdale, CA., January 11, 1999		
	T			
Examiner	ici	Date Considered 1/18/06		

Pg. 1 of 1